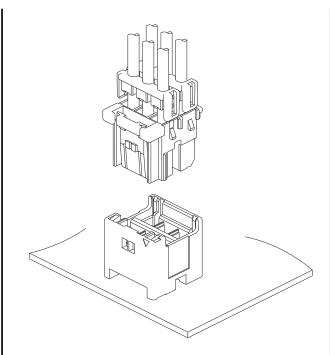


HYV CONNECTOR

5.0 mm pitch/Wire-to-Board connectors/Crimp style and Mating style



This is a 5.0 mm pitch wire-to-board connector with a mechanism to prevent incomplete mating.

The box-shaped structure of the contacts, which prevents the influence of external force, enhances the safety of high-current circuit connections, and the four types of key patterns prevent incorrect mating when using more than one piece of the same circuit types in narrow areas.

- Keying structure
- Secure lock mechanism
- · Inertia lock mechanism
- Retainer mountable type
- Suitable for potting process

Specifications

- Current rating: 10 A AC/DC (2 circuits/ AWG #16)
 - ** The following table shows the rated current when applying current for all circuits in each combination of the number of circuits and the wire to be used.

Unit: A

No. of	Wire size (AWG)			
circuits	#16	#18	#20	#22
2	10	7	5	3
3	9	7	5	3
4	9	7	5	3
6	8	7	5	3

Note) Notes on parallel branching current:

Do not branch to the multiple circuits in parallel current which is exceeds the rated current, as it may cause problems such as imbalance when applying current.

If it is unavoidable that branch in parallel is necessary, design the circuits while suppressing the unbalanced current and proving the sufficient margin to the rated current.

- Voltage rating: 300 V AC/DC
- Temperature range: -30°C to +105°C

(including temperature rise in applying electrical current)

· Contact resistance:

Initial value/ $10 \text{ m}\Omega$ max.

After environmental tests/ 20 m Ω max.

- Insulation resistance: 1,000 M Ω min.
- Withstanding voltage:

There shall be no breakdown or flashover while applying 1,500 VAC for one minute.

· Applicable wire range:

Conductor size 21 type/ AWG #22 to AWG #18

41 type/ AWG #18 to AWG #16

Insulation O.D. 21 type/ ϕ 1.8 mm to ϕ 2.7 mm

41 type/ ϕ 2.3 mm to ϕ 3.0 mm

 $(\phi 2.9 \text{ mm max. upon})$

using a retainer)

- · Applicable PC board thickness: 1.6 mm
- * Please refer to the "Handling Precautions for Terminals and Connectors" on our website (listed in the "Technical Documents" column on the Product Information page) before use.
- * RoHS2 compliance
- * Dimensional unit: mm
- * Contact JST for details.

Standards

For information on overseas standard registrations, please refer to the "List of Registered Overseas Standards" on our website (listed in the "Technical Documents" column on the Product Information page).

* Specifications registered to overseas standards may differ from the general specifications listed above.

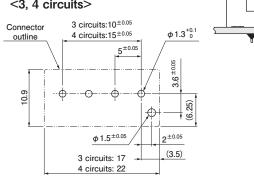
PC board layout and Assembly layout

Single-row

<2 circuits>

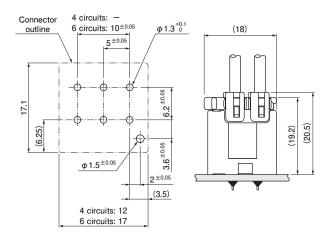
Connector $\phi 1.3^{+0.1}$ outline 5 ±0.05 $\mathbf{3.6} \pm 0.05$ (11.8) 10.9 2^{±0.05} $\phi 1.5^{\pm 0.05}$ $\phi 1.5^{\pm 0.05}$ (3.5) 0 (12

<3, 4 circuits>



Dual-row

<4, 6 circuits>



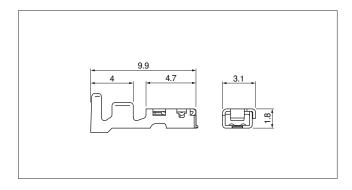
Note: 1. The figure of PC board layout is the figure viewed from the connector mounting side.

2. Tolerance for the PCB hole pitch shall be \pm 0.05, and shall not accumulate more than \pm 0.05.

(20.5) (19.2)

3. Hole dimensions differ according to the type of PC board and piercing method. The above dimensions are reference values. Please contact JST for details.

Socket contact



Model No.	Applicable wire range			
woder no.	Conductor size AWG (mm²)	Insulation O.D. (mm)	reel	
SHYVF-21T-M0.5	#22 to #18 (0.33 to 0.83)	1.8 to 2.7	4,000	
SHYVF-41T-M0.5	#18 to #16 (0.83 to 1.25)	2.3 to 3.0	3,000	

Material and Surface finish, etc.

Copper alloy, tin-plated

Note: The insulation O.D. of the applicable wire shall be ϕ 2.9 mm max. upon using a retainer of 41-barrel contact.

Crimping machine

Contact	Crimping machine	Applicator	Crimp applicator with dies
SHYVF-21T-M0.5	AP-K2N	MKS-L	APLMK SHYVF21-05
SHYVF-41T-M0.5	AF-NZIN		APLMK SHYVF41-05

Note: Contact JST for fully automatic crimping applicator.

Socket housing

Single-row <2 circuits> <3 circuits> 16.6 10 11.6 1.5 5.5 1.5 Mark of Mark of 5 No.1 circuit No.1 circuit <4 circuits> 1.5 Mark of No.1 circuit **Dual-row** <4 circuits> <6 circuits> 16.6 11.6 11.7 1.5 11.7 1.5 10 Mark of Mark of 5 No.1 circuit No.1 circuit

	Type	No. of circuits	Model No.	Q'ty/bag
		2	HYVRP-02V-A-S	1,000
	Single-row	3	HYVRP-03V-A-S	1,000
		4	HYVRP-04V-A-S	600
	Dual rour	4	HYVRP-04V-W-A-S	600
Dual-row	Duai-row	6	HYVRP-06V-W-A-S	600

Material and Surface finish, etc.

PA 66 (Glass-filled), natural

Note: 1. Contact JST for key patterns.
2. For flame retardant grade of resin material used, please refer to the "List of Registered Overseas Standards" on our website (listed in the "Technical Documents" column on the Product Information page).

Header

Single-row <3 circuits> <4 circuits> <2 circuits> 12 10.9 10.9 10.9 Mark of No.1 circuit Mark of No.1 circuit Mark of No.1 circuit 9 9 3.6 3.6 3.6 10 **Dual-row** <6 circuits> <4 circuits> 17.1 17 17.1 Mark of Mark of No.1 circuit No.1 circuit 16 16 3.8 3.8 3.6 6.2 3.6 6.2 10

Type	No. of circuits	Model No.	Q'ty/box
Single-row	2	B02B-HYVSS-A-1	150
	3	B03B-HYVSS-A-1	100
	4	B04B-HYVSS-A-1	100
Dual-row	4	B04B-HYVSS-A-1W	100
	6	B06B-HYVSS-A-1W	50

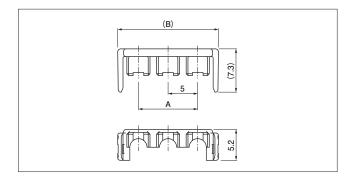
Material and Surface finish, etc.

Press post: Copper alloy, copper-undercoated, tin-plated Wafer: PA 66 (Glass-filled)

Note: 1. Contact JST for key patterns.

For flame retardant grade of resin material used, please refer to the "List of Registered Overseas Standards" on our website (listed in the "Technical Documents" column on the Product Information page).

Retainer



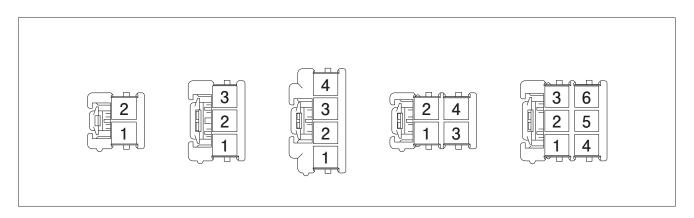
No. of	Model No.	Dimensions (mm)		Q'ty/bag
circuits		Α	В	Q ty/bag
2	HYVS-02V	_	12.0	2,000
3	HYVS-03V	10.0	17.0	2,000
4	HYVS-04V	15.0	22.0	1,000

Material and Surface finish, etc.

PA 66 (Glass-filled), natural

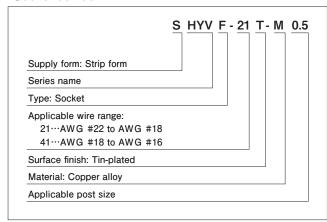
Note: For flame retardant grade of resin material used, please refer to the "List of Registered Overseas Standards" on our website (listed in the "Technical Documents" column on the Product Information page).

Housing position location numbers

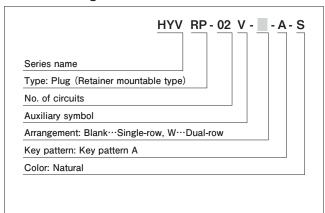


Model number allocation

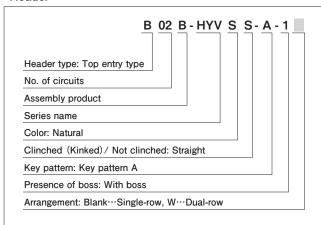
Socket contact



Socket housing



Header



Retainer

